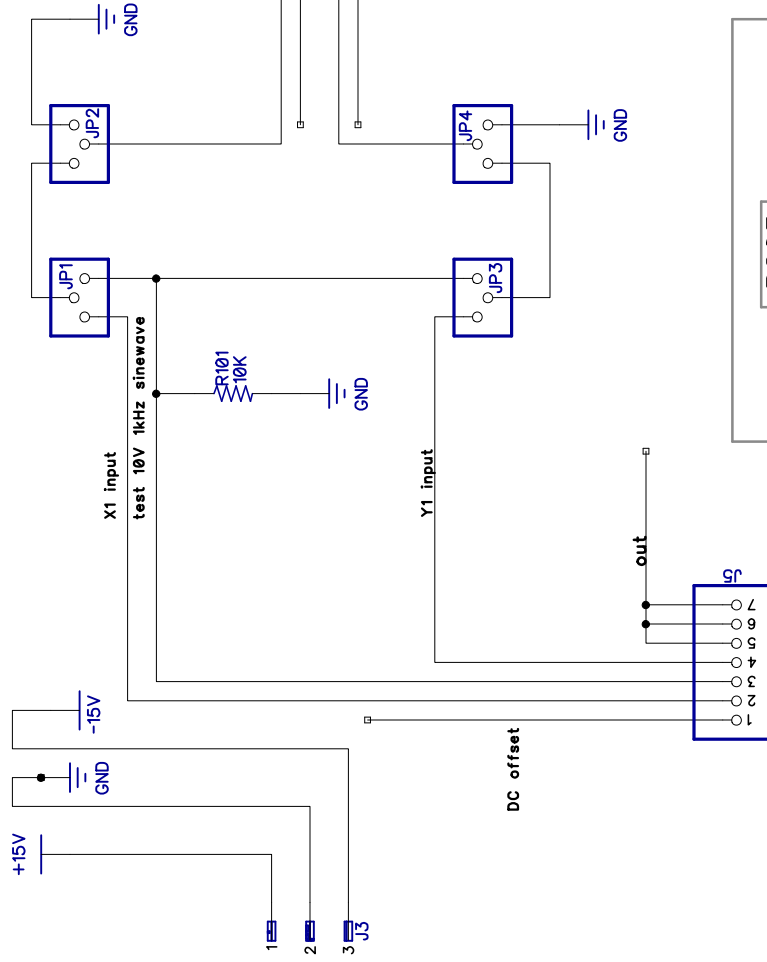


These AD633 pinouts are those of the SOIC part, not the DIP.

redundant power connections to allow daisy-chaining.



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from <http://www.glensstuff.com/spacfcss/schematics/multsch.pdf>

trim procedure

jumpers 1, 2, and 3 to right
jumper 4 to left

apply 10V 1 kHz sine wave

trim R203 for minimum AC out

jumper 4 to right

jumper 2 to left

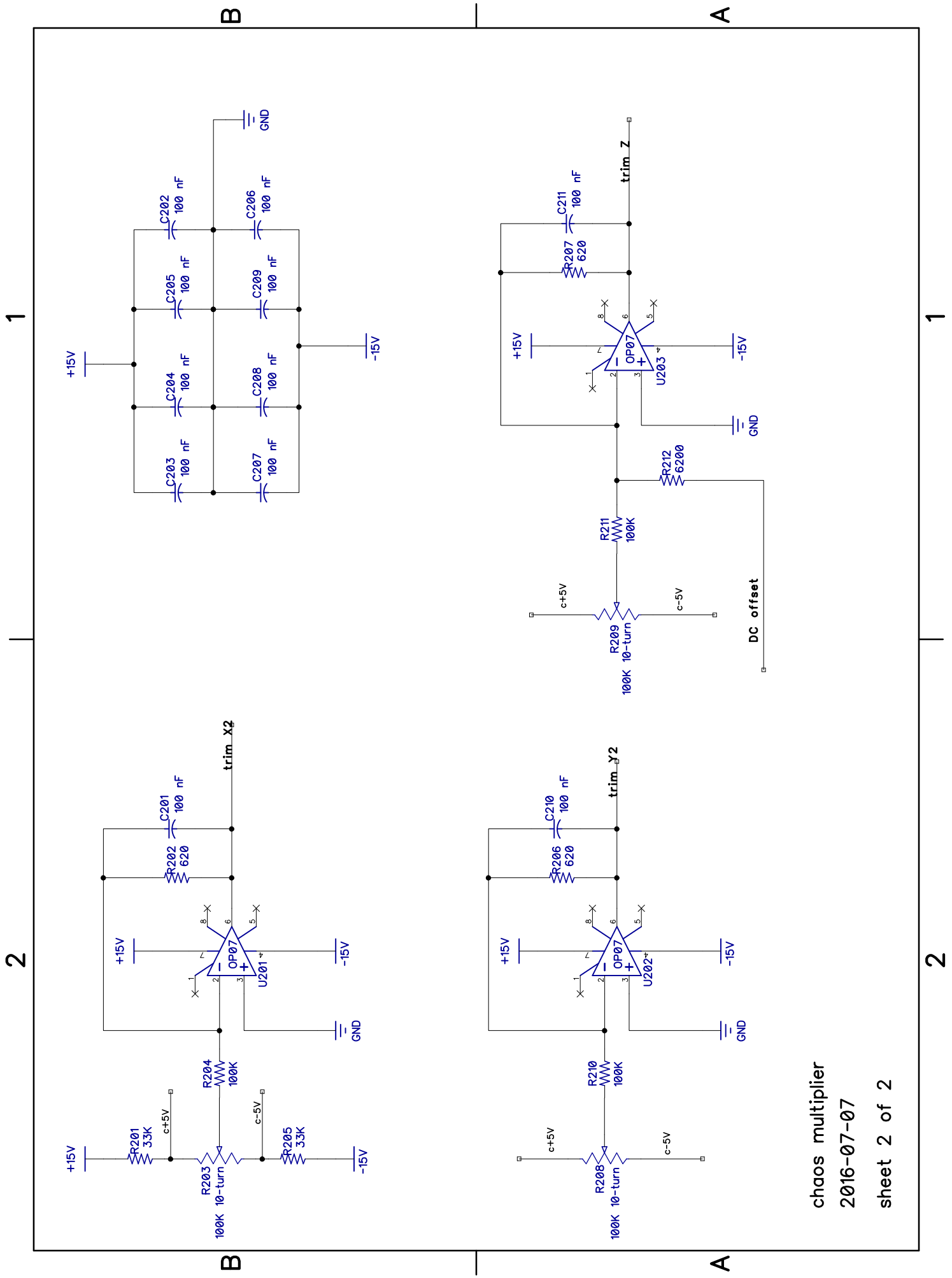
trim R208 for minimum AC out

remove sine wave

jumper 2 to right

trim R209 for minimum DC offset out

all jumpers to left



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